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19 APR 2001

Docket No.: 50212-211

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of

Kenichi ARIMURA, et al.

Serial No.:

Group Art Unit:

Filed: April 19, 2001

Examiner:

For: WAFER SUPPORT IN SEMICONDUCTOR PRODUCTION APPARATUS

**INFORMATION DISCLOSURE STATEMENT**

Honorable Commissioner for Patents and Trademarks  
Washington, D. C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed with the application and no certification or fee is required.

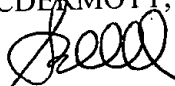
A copy of the foreign search report is attached for the Examiner's information. Please note this is a PCT application in the entry of the National Phase in the U.S. Since the Search Report was from the U.S. JPO or EPO search authorities, copies of these references should have been supplied to the USPTO under the trilateral agreement and are believed to be in the file of the above identified application and readily available to the Examiner.

Serial No.:

Therefore it is believed that Applicants have met all requirements regarding duty of disclosure under 37 CFR 1.56. Acknowledgement and consideration of these documents are respectfully requested.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

  
Stephen A. Becker  
Registration No. 26,527

600 13<sup>th</sup> Street, N.W.  
Washington, DC 20005-3096  
(202) 756-8000 SAB:klm  
**Date: April 19, 2001**  
Facsimile: (202) 756-8087